

## Learn to program an Apple 1 – Beginner's Instructions

Follow these instructions to learn how to use an Apple 1 (Replica 1 Plus) computer. Note: These procedures can also be run using Apple 1 emulator software, such as Open Emulator.

1. Turn on the Replica 1 Plus using the switch in the upper left corner of the board. The monitor should display random characters on the screen.
2. Press the RESET button on the middle left of the Replica 1 board. You should see a flashing @ sign at the bottom of the screen.
3. Press the CLEAR button on the middle right of the Replica 1 board. The screen should clear.
4. Press the ENTER key (also known as the Return key) a few times.
5. Type the following, then press ENTER:

**300.30F**

6. The computer should display 2 lines of memory. The first memory address is \$0300 in hexadecimal (base 16), which corresponds to 768 in decimal ( $16*16*3$ ). The second memory address is \$030F. The letter F represents the number 15, so the address = 768 + 15 or 783.
7. Enter the following hex codes to store a program at address \$300. If you make a mistake, press ENTER and retype the line. (Note: The underscore character “\_” can be used as a backspace, but the screen will not back up and erase the previous character – it will internally delete it in memory.

**300:A9 00 AA 20 EF FF E8 8A 4C 02 03**

8. The computer will respond by showing you the previous contents of address \$300 before you changed it to \$A9. Type the following to verify your work:

**300.30F**

9. If you entered all the codes for the program correctly, try running it by typing:

**300R**

10. If the program runs successfully you will see all the Ascii characters displayed in order on the monitor. The sequence will repeat until it fills the screen. Notice that there are no lower case letters. Lowercase did not become standard until the Apple //e was released in 1983. Earlier Apple ][ computers required special hardware for entry and display of lowercase. Now is a good time to get a photo with your first Apple 1 program running successfully!

To understand what the codes you typed mean, examine the following listing. The addresses and hex codes you typed are in the left column. The middle column contains machine language instructions that a programmer uses to tell the computer exactly what to do. The right column contains comments explaining what the instructions do.

	ORG \$0300	; Enter program at address \$300 (768)
300:A9 00	LDA #\$00	; Store 0 in the Accumulator register
302:AA	TAX	; Transfer A to the X register
303:20 EF FF	JSR \$FFEF	; Call a subroutine to print the ; character in the A register.
306:E8	INX	; Add 1 to the X register
307:8A	TXA	; Transfer the X to the A register.
308:4C 02 03	JMP \$302	; Jump to address \$302

Now you will learn how to write a program using Apple BASIC. The Apple 1 used a version of the BASIC language written by Steve Wozniak, which only supported Integer values – it did not have decimals. Also, the number range was limited to values between -32767 and +32767.

1. Push RESET, then CLEAR. Then press ENTER a few times.
2. Type the following command:  
**E000 R**
3. You should see the prompt: > This means that BASIC is running.
4. Press ENTER a few times.
5. Enter the following lines. Press ENTER at the end of each line. Fill in your name where it says "your name" below.

```
10 X=1 : Y=1
20 TAB(X) : PRINT "HELLO your name"
30 X=X+Y : IF X = 10 THEN Y = -1
40 IF X > 0 THEN 20
50 END
```

6. Type: **LIST** and press ENTER to verify that you typed each line correctly.
7. Type: **RUN** and press ENTER. If the program works, you will see your name printed 19 times forming an arrow pattern on the screen. Congratulations! Take another photo. If you want to learn more, continue with the Advanced worksheet on the next page.

*The Replica 1 Plus computer is sold by ReActiveMicro.com. It was designed by Vince Briel (brielcomputers.com).*

*The WozPak Special Edition book is sold by www.CallApple.org.*

*This public domain document was written by Eric Rangell (erangell@gmail.com).*



4. Look up the opcode for LDX Immediate addressing and put it in the third box. Enter the hex value for the number 1 in the 4<sup>th</sup> box. The "OLOOP" is a label that means "outer loop", which is only for human reference, so we can refer to this LDX instruction later. The X and Y registers are called Index registers because they are typically used to calculate offsets from a memory address, as we will see next.
5. The label "ILOOP" means Inner Loop. For each of the 20 lines (the Outer Loop), we need to print all the characters of our message (the Inner Loop). The instruction LDA \$340,X means to load the A register (also known as the Accumulator) with the value from memory address calculated as \$340 plus the value of the X register. Look up the opcode for the Indexed Absolute addressing mode that uses the X register. Since X currently equal 1, it will load the A register with the memory value from address \$341, which contains the Ascii value of the 'H' character in the word "HELLO". In order to tell the computer the address \$340, it needs to be stored in 2 bytes, with the LOW byte of the address first, followed by the HIGH byte. For \$340, the LOW byte is \$40 and the HIGH byte is \$03. This needs to be done because the value \$340 is greater than the largest number that can fit in 1 byte: \$FF or 255 decimal.
6. The JSR \$FFEF instruction calls a program written by Steve Wozniak in 1977, which is hard wired in the ROM of the Apple 1. When you call that program (known as a subroutine) the Ascii character in the Accumulator gets printed to the monitor (or TV, used by most Apple 1 users in 1977). After you lookup the JSR opcode (which means Jump to SubRoutine), remember to put the LOW byte of the \$FFEF address first, followed by the HIGH byte.
7. Look up the INX (Increment X) opcode. It is a 1 byte instruction.
8. Look up the CPX opcode for the Absolute addressing mode. This instruction means "ComPare X to something". Then encode the address \$0340, LOW byte followed by HIGH byte. When the comparison is done, flags are set in the 6502 Processor Status register to indicate the result of the compare, as follows:
  - a. If X is less than the value, the C (carry) flag is clear (0).
  - b. If X is greater than or equal to the value, the C (carry flag is set (1)).
  - c. If X exactly equals the value, the Z (zero) flag is set (1).
  - d. If X does not exactly equal the value, the Z (zero) flag is clear (0).

9. The instruction BCC ILOOP is a Branching instruction, which means that if the specified condition is met, the next instruction executed by the CPU will be the instruction at the specified label. If the condition is not met, the program flow will simply continue with the next instruction. BCC means Branch if Carry is Clear, so if the X register is less than the value in memory address \$340, this instruction will branch to the instruction at the ILOOP label. However, the 6502 doesn't know what ILOOP is. Instead it needs to know how many bytes to go forwards or backwards from the current instruction. So count the number of boxes starting at the 2<sup>nd</sup> byte of the BCC ILOOP instruction, and going back until you reach the 1<sup>st</sup> byte of the LDA \$340 instruction. Then look up that negative number in the following table to get the value for the 2<sup>nd</sup> byte of the BCC ILOOP instruction. This addressing mode is called Relative Addressing.

Decimal	-1	-2	-3	-4	-5	-6	-7	-8	-9
Hex	FF	FE	FD	FC	FB	FA	F9	F8	F7

Decimal	-10	-11	-12	-13	-14	-15	-16	-17	-18
Hex	F6	F5	F4	F3	F2	F1	F0	EF	EE

Decimal	-19	-20	-21	-22	-23	-24	-25	-26	-27
Hex	ED	EC	EB	EA	E9	E8	E7	E6	E5

10. The instruction BEQ ILOOP means Branch if Equal. It checks the Z register. If it equals 1, the branch is taken. Otherwise, (if Z=0) it continues with the next instruction. After you look up the opcode for BEQ, use the table above to find the offset. Note that the combination of the BCC and BEQ instructions are needed to check if X is less than or equal to the value in \$340.
11. Look up the opcode for the LDA immediate instruction. The value \$8D is the hi-bit Ascii value for a Carriage Return. Apple computers use Ascii values between 128 and 255. The Return key on the Apple is the same as Control M. M is the 13<sup>th</sup> letter of the alphabet, which has the hex value \$D. Therefore, the Apple value for the carriage return is \$8D which equals 128+13 or 141.
12. The same JSR instruction we have seen before is being called to output the Carriage Return character. This will return the cursor to the first character position on the next line, where we will continue printing the next line of text.

13. Look up the opcode for DEY - it means DEcrement the Y register, which subtracts 1 from Y. Then for the BNE OLOOP instruction, count the boxes and lookup the offset to get back to the OLOOP instruction.
14. Look up the opcode for RTS - it means ReTurn from Subroutine. This will return to the caller of our program (which may be another machine language program, the Apple monitor, or a Basic program that called our subroutine).
15. When you have filled in all the opcodes, and have looked up the Ascii hex values for your name, you are ready to type your program into the Replica 1 Plus and run it. See the Beginner's Instructions for details about how to type in hex codes for a program. When you have entered and verified your program, you can run it using the command: **300 R**

If your program runs successfully, Congratulations! Take a photo with your program and its output.

If not, don't be discouraged. Most machine language programs do NOT work on the first try. Now you will learn how to troubleshoot your program.

- a. Type the following command to display the hex bytes for your program: **300.340**
- b. Verify that you typed all the hex codes correctly for your program.
- c. If you are sure that you typed in all the codes correctly, run a disassembler program so you can see how the computer interpreted your hex codes. If the instructions shown do not match exactly with the listing on the first page, determine which bytes were incorrectly entered. Make a list of the addresses and values that were incorrect. You will then patch them with the correct values and try again.
- d. If you get stuck, ask a friend to take a look at your program. Another set of eyes often finds bugs.
- e. Sometimes when you are writing your own programs and you get stuck it may make sense to start over. There are also tools that can help you step through your program as it runs and examine memory locations and register values.

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Add with Carry  
 01  
 + 11  
 ---  
 100

00 → 0  
 01 → 0  
 10 → 0  
 11 → 1

Shift Left

BRANCHES

CLEAR

Compare

Processor Status Flags



ADC Imm	69	CMP ZP, X	D5	LDA Abs	AD	ROR Acc	6A
ADC ZP	65	CMP Abs	CD	LDA Abs, X	BD	ROR ZP	66
ADC ZP, X	75	CMP Abs, X	DD	LDA Abs, Y	B9	ROR ZP, X	76
ADC Abs	6D	CMP Abs, Y	D9	LDA (Indir, X)	A1	ROR Abs	6E
ADC Abs, X	7D	CMP (Indir, X)	C1	LDA (Indir, Y)	B1	ROR Abs, X	7E
ADC Abs, Y	79	CMP (Indir), Y	D1	LDX ZP	A6	RTI	40
ADC (Indir, X)	61	CPX Imm	E0	LDX ZP, Y	B6	RTS	60
ADC (Indir), Y	71	CPX ZP	E4	LDX Abs	AE	SBC Imm	E9
AND Imm	29	CPX Abs	EC	LDX Abs, Y	BE	SBC ZP	E5
AND ZP	25	CPY Imm	C0	LDX Imm	A2	SBC ZP, X	F5
AND ZP, X	35	CPY ZP	C4	LDY Imm	A0	SBC Abs	ED
AND Abs	2D	CPY Abs	CC	LDY ZP	A4	SBC Abs, X	FD
AND Abs, X	3D	DEC ZP	C6	LDY ZP, X	B4	SBC Abs, Y	F9
AND Abs, Y	39	DEC ZP, X	D6	LDY Abs	AC	SBC (Indir, X)	E1
AND (Indir, X)	21	DEC Abs	CE	LDY Abs, X	BC	SBC (Indir), Y	F1
AND (Indir), Y	31	DEC Abs, X	DE	LSR Acc	4A	SEC	38
ASL Acc	0A	DEX	CA	LSR ZP	46	SED	F8
ASL ZP	6	DEY	88	LSR ZP, X	56	SEI	78
ASL ZP, X	16	EOR Imm	49	LSR Abs	4E	STA ZP	85
ASL Abs	0E	EOR ZP	45	LSR Abs, X	5E	STA ZP, X	95
ASL Abs, X	1E	EOR ZP, X	55	NOP Do Nothing!	EA	STA Abs	8D
BCC	90	EOR Abs	4D	ORA Imm	9	STA Abs, X	9D
BCS	B0	EOR Abs, X	5D	ORA ZP	5	STA Abs, Y	99
BEQ	F0	EOR Abs, Y	59	ORA ZP, X	15	STA (Indir, X)	81
BIT ZP	24	EOR (Indir, X)	41	ORA Abs	0D	STA (Indir), Y	91
BIT Abs	2C	EOR (Indir), Y	51	ORA Abs, X	1D	STX ZP	86
BMI	30	INC ZP	E6	ORA Abs, Y	19	STX ZP, Y	96
BNE	D0	INC ZP, X	F6	ORA (Indir, X)	1	STX Abs	8E
BPL	10	INC Abs	EE	ORA (Indir), Y	11	STY ZP	84
BRK	0	INC Abs, X	FE	PHA PUSH	48	STY ZP, X	94
BVC	50	INX	E8	PHP ON STACK	8	STY Abs	8C
BVS	70	INY	C8	PLA PULL	68	TAX	AA
CLC	18	JMP Indir	6C	PLP FROM STACK	28	TAY	A8
CLD	D8	JMP Abs	4C	ROL Acc	2A	TSX	BA
CLI	58	JSR	20	ROL ZP	26	TXA	8A
CLV	B8	LDA Imm	A9	ROL ZP, X	36	TXS	9A
CMP Imm	C9	LDA ZP	A5	ROL Abs	2E	TYA	98
CMP ZP	C5	LDA ZP, X	B5	ROL Abs, X	3E		



Return from Interrupt

Subtract with Carry (Borrow)

Set Flags



Transfers



opcodes

6502 OPCODES / (Operation Codes) Instructions

Apple uses High Bit ASCII  
 The leftmost bit = 1  
 So add 128 to decimal Value

When coding your name:  
 4x → Cx

### Decimal - Binary - Octal - Hex - ASCII Conversion Chart

Add \$80 to Hex value

Decimal	Binary	Octal	Hex	ASCII	Decimal	Binary	Octal	Hex	ASCII	Decimal	Binary	Octal	Hex	ASCII
0	00000000	000	00	NUL	32	00100000	040	20	SPACE	64	01000000	100	40	@
1	00000001	001	01	SOH	33	00100001	041	21	!	65	01000001	101	41	A
2	00000010	002	02	STX	34	00100010	042	22	"	66	01000010	102	42	B
3	00000011	003	03	ETX	35	00100011	043	23	#	67	01000011	103	43	C
4	00000100	004	04	EOT	36	00100100	044	24	\$	68	01000100	104	44	D
5	00000101	005	05	ENQ	37	00100101	045	25	%	69	01000101	105	45	E
6	00000110	006	06	ACK	38	00100110	046	26	&	70	01000110	106	46	F
7	00000111	007	07	BEL	39	00100111	047	27	'	71	01000111	107	47	G
8	00001000	010	08	BS	40	00101000	050	28	(	72	01001000	110	48	H
9	00001001	011	09	HT	41	00101001	051	29	)	73	01001001	111	49	I
10	00001010	012	0A	LF	42	00101010	052	2A	*	74	01001010	112	4A	J
11	00001011	013	0B	VT	43	00101011	053	2B	+	75	01001011	113	4B	K
12	00001100	014	0C	FF	44	00101100	054	2C	,	76	01001100	114	4C	L
13	00001101	015	0D	CR	45	00101101	055	2D	-	77	01001101	115	4D	M
14	00001110	016	0E	SO	46	00101110	056	2E	.	78	01001110	116	4E	N
15	00001111	017	0F	SI	47	00101111	057	2F	/	79	01001111	117	4F	O
16	00010000	020	10	DLE	48	00110000	060	30	0	80	01010000	120	50	P
17	00010001	021	11	DC1	49	00110001	061	31	1	81	01010001	121	51	Q
18	00010010	022	12	DC2	50	00110010	062	32	2	82	01010010	122	52	R
19	00010011	023	13	DC3	51	00110011	063	33	3	83	01010011	123	53	S
20	00010100	024	14	DC4	52	00110100	064	34	4	84	01010100	124	54	T
21	00010101	025	15	NAK	53	00110101	065	35	5	85	01010101	125	55	U
22	00010110	026	16	SYN	54	00110110	066	36	6	86	01010110	126	56	V
23	00010111	027	17	ETB	55	00110111	067	37	7	87	01010111	127	57	W
24	00011000	030	18	CAN	56	00111000	070	38	8	88	01011000	130	58	X
25	00011001	031	19	EM	57	00111001	071	39	9	89	01011001	131	59	Y
26	00011010	032	1A	SUB	58	00111010	072	3A	:	90	01011010	132	5A	Z
27	00011011	033	1B	ESC	59	00111011	073	3B	;	91	01011011	133	5B	[
28	00011100	034	1C	FS	60	00111100	074	3C	<	92	01011100	134	5C	\
29	00011101	035	1D	GS	61	00111101	075	3D	=	93	01011101	135	5D	]
30	00011110	036	1E	RS	62	00111110	076	3E	>	94	01011110	136	5E	^
31	00011111	037	1F	US	63	00111111	077	3F	?	95	01011111	137	5F	_

Notice 0-15 Hex  
 10-15 Hex  
 0000-1111 Binary  
 Line Feed  
 Carriage Return

Control Characters

5x → Dx